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INFORMATION DISCLOSURE STATEMENT

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1875.0230000APPLICATION NO.
09 753,664APPLICANT
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January 4, 2001GROUP
2823

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
KN	AA1	5,162,258	11/10/92	Lemnios et al.	437	184	3/20/90
KN	AB1	5,792,704	8/11/98	Jun et al.	438	624	1/4/96
KN	AC1	5,972,192	10/26/99	Dubin et al.	205	101	7/23/97
KN	AD1	5,990,015	11/23/99	Lin et al.	438	706	3/12/98
KN	AE1	6,048,762	4/11/00	Hsia et al.	438	238	4/6/98
	AF1						
	AG1						
	AH1						
	AI1						
	AJ1						
	AK1						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
	AL1						Yes No
	AM1						Yes No
	AN1						Yes No
	AO1						Yes No
	AP1						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

KN.	AR	1	Smith, "Application-Specific Integrated Circuits," 8 th Edition, May, 2000, Chapter 4, Programmable ASICs, 4.1 The Antifuse, pages 170-174.
KN.	AS	1	Institute of Microelectronics, "Deep Submicron - Advanced RFIC Integration," IME Strategic Focuses, http://www.ime.org.sg/deep/deep_rfic.htm , 6/27/2002, pages 1-3.
	AT	1	

EXAMINER

Khuenngruen

DATE CONSIDERED

9/16/02

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.